

10/58378

Attorney Docket No. XA-10583  
AP20 Recd PCT/PTO 21 JUN 2006

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:

First Named Inventor: OSHIBA, MASASHI

**Art Unit:**

Intl. Appln. No.: PCT/JP2004/014939

**Examiner:**

Intl. Filing Date: 08 October 2004

**Confirmation No.:**

For: SEMICONDUCTOR INTEGRATED CIRCUIT APPARATUS AND  
ELECTRONIC SYSTEM

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attn: Mail Stop PCT, DO/EO/US

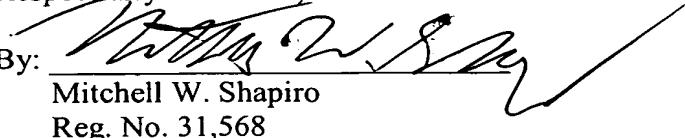
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Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO/SB/08A are hereby cited.

Document 3 on the attached List was cited in the specification, on page 2, and its relevance is indicated therein. Document 3 is a counterpart of U.S. Document 1. Documents 2, 4, and 5 were cited in the International Search Report (copy attached).

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Respectfully submitted,

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Sheet 1 of 1

## Complete it Known

10/583778

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**First Named Inventor**

## **Art Unit**

**Examiner Name**

**Attorney Docket Num**

**OSHIBA, Masashi**

## U.S. PATENT DOCUMENTS

## **FOREIGN PATENT DOCUMENTS**

**Examiner Signature** \_\_\_\_\_ **Date Considered** \_\_\_\_\_

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